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STATEMENT B	Y APPLICANT

Complete if Known 10/684,053 Application Number Filing Date October 9, 2003 First Named Inventor Chandan Mathur Art Unit 2183 Examiner Name

(Use as many sheets as necessary)

of

Substitute for form 1449A/PTO

Attorney Docket Number 1934-12-3

			U.S. PATENT	OCUMENTS		
Examiner Cite		Document Number	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant	
	Cite No. ¹	Number • Kind Code ² (if known)	MM-DD-YYYY	Cited Document	Passages or Relevant Figures Appear	
/DJH/		US-5,583,964	12/10/96	Wang		
/DJH/		US- 4,956,771	09/11/90	Neustaedter		
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		FOREIGN PA	TENT DOCU	MENTS		
Examiner	Cite	Foreign Patent Document Public		Name of Patentee or Applicant of Cited	Pages, Columns, Lines, Where Relevant	
Initials*	No.1	Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)	Date MM-DD-YYYY	Document	Passages or Relevant Figures Appear	T⁰
/DJH/		EP 1 061 438 A1	12/20/00	Hewlett- Packard Company	•	
/DJH/		EP 0 945 788 A2	09/29/99	lexas Instruments Incorporated		
/DJH/		EP 0 945 788 A3	06/26/02	Texas Instruments Incorporated	·	
/DJH/		EP 0 694 847 A2	01/31/96	International Business Machines Corporation		
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Examiner Signature	/David Huisman/	Date Considered	06/21/2007	

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Substitute	for form 1449	B/PTO	•			Complete if Known
INIEO	DILATI	0N DI		COURT	Application Number	10/684,053
				LOSURE	Filing Date	October 9, 2003
STAT	LEWEN	TBY A	APF	PLICANT	First Named Inventor	Chandan Mathur
					Art Unit	2183
	(Use as mai	ny sheets	as nec	essary)	Examiner Name	
Sheet	2	0	f 2		 Attorney Docket Number	1934-12-3

		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials *	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
/DJH/		LECURIEUX-LAFAYETTE G: "Un Seul FPGA Dope Le Traltement D'Images", Electronique, CEP Communication, Paris, FR, no. 55, 1996, pp. 98, 101-103	
/DJH/		VERMEULEN F. ET AL., "Flexible Hardware Acceleration for Multimedia Oriented Microprocessors", Micro-33. Proceedings of the 33 rd Annual ACM/IEEE International Symposium on Microarchitecture. Monterey, CA, Dec. 10-13, 2000, Proceedings of the Annual ACM/IEEE International Symposium on Microarchitecture, Los Alamitos, CA: IEEE Comp. Soc, US, 10 December 2000, pp. 171-177	
/DJH/		International Search Report for PCT/US 03/34557, December 21, 2004	
/DJH/		International Search Report for PCT/US 03/34559, January 7, 2005	
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1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.

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				Application Number	10/684,053	
INFO	PRMATION	DIS	CLOSURE	Filing Date	October 9, 2003	
STA	TEMENT B	ΥA	PPLICANT	First Named Inventor	Chandan Mathur	
7				Art Unit	2183	
	(Use as many she	eets as	necessary)	Examiner Name		
Sheet	1	of	2	Attorney Docket Number	1934-12-3	

			U.S. PATENT	OCUMENTS	
Examiner	Cito	Document Number	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevan
Initials * No.1	No.1	Number - Kind Code ² (if known)	MM-DD-YYYY	Cited bottiment	Pages, Columns, Lines, Where Relevan Passages or Relevant Figures Appear
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	FOREIGN PATENT DOCUMENTS						
Examiner Cite	Cito	Foreign Patent Document	Publication	Name of Patentee or	Pages, Columns, Lines, Where Relevant		
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Examiner Signature	David Huisman/	Date Considered	06/21/2007

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Complete if Known Application Number 10/684,053 October 9, 2003 Filing Date Chandan Mathur First Named Inventor 2183 Art Unit Examiner Name Attorney Docket Number 1934-12-3

(Use as many sheets as necessary) of 2 Sheet

	NON PATENT LITERATURE DOCUMENTS					
Examiner Initials *	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²			
/DJH/		International Search Report for PCT/US2005/035818 dated August 4, 2006				
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Examiner Signature	/David Huisman/	Date Considered	06/21/2007

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Application Number	10/684;053			
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	,	NON PATENT LITERATURE DOCUMENTS		
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/DJH/	BAKSHI S; GAJSKI D D; "Partitioning and Pipelining for Performance-Constrained Hardware/Software Systems", IEEE Transactions on Very Large Scale Integration (VLSI) Systems, Vol. 7, NR. 4, PG. 419-432, (1999-12-00), XP000869229			

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